

Google scholar

trailer pointer "packet memory" "IP header"

Search

Advanced Scholar Search

ATM reassembly controller and method

DA Thompson - US Patent 5,917,828, 1999 - Google Patents
 ... 606-^ \ PAD \ CPCS - 00 \ \ LENGTH \ CRC - 32 PAYLOAD CPI 0-65535 0-47 1 1 2 4 \ / 620 -
 CPCS **TRAILER** 800- PRIOR ... V^ J DETERMINE VCN FROM VPC AND VCI -1204 FETCH RCTE
 TO DETERMINE VC 1208 1210- -1206 LOCAL MEMORY **POINTER** VALID (STATE 1 ...

Cited by 41 - Related articles - All 2 versions

Method and apparatus for dynamic packet batching with a high performance network interface

S Muller... - US Patent 6,483,804, 2002 - Google Patents
 ... **HEADER** 210 LAYER TWO **HEADER** 212 LAYER THREE **HEADER** 214 LAYER FOUR **HEADER**
 216 DATA PORTION 202 **TRAILER** 206 **PACKET** ... 19,2002 Sheet 14 of 49 US 6,483,804 BI
PACKET QUEUE 116 **PACKET** # ENTRY 800- READ **POINTER** 810 WRITE **POINTER** ...

Cited by 34 - Related articles - All 4 versions

Apparatus and method of analyzing internet activity

TD McCreery... - US Patent 5,787,253, 1998 - Google Patents
 ... Destination: 00:00:94:05:d9:00 574^ Source: 08:00:07:3c:00:46 Protocol Type:0x0800 IP **IP**
Header - internet Protocol ... Number: 1155984753 Offset: 5 Reserved: %000000 Code: %011000
 Ack is valid j Push Request Window: 11557 Checksum: 0x5ae4 Urgent **Pointer**: 0 No ...

Cited by 154 - Related articles - All 2 versions

Table driven programming system for a services processor

AE Henderson... - US Patent App. 10/359,041, 2003 - Google Patents
 ... 18,2003 Sheet 6 of 7 US 2003/0231634 AI CN CD .a CO t- CM c d c. C 0 g "o "o "CD "o .Q Z3
 CO ~ ^ — CN .CD CD _Q _Q CD CD **~ CN I— C C CD .g 0 g 2 "o "o "o £ ^ Til >^ C CD CD
 ~z. CO CD ^_ CN CO -Q 03 c _g C .O c _g 1— ^t-» "CD "CD CD 3 E E E iv 5 £ L_ P p o ...

Related articles - All 5 versions

Method and apparatus for data re-assembly with a high performance network interface

S Muller, DE Gentry Jr, LT Cheng... - US Patent 6,480,488, 2002 - Google Patents
 ... **ONE** **HEADER** 210 LAYER TWO **HEADER** 212 LAYER THREE **HEADER** 214 LAYER FOUR
HEADER 216 DATA PORTION 202 **TRAILER** 206 **PACKET** 200 ... 12,2002 Sheet 14 of 49 US
 6,480,489 BI ENTRY 80 READ **POINTER** 810 WRITE **POINTER** 812 **PACKET** QUEUE 116 ...

Cited by 65 - Related articles - All 4 versions

Methods and devices for transmitting data between storage area networks

A Goishan, N Mehta, P Krishnamoorthy... - US Patent App. 10/... , 2003 - Google Patents
 ... PS p^~ re o 2 N^« O O s cr re re o o Rv \ O I—i Ji. ... 10 illustrates a remaining **pointer** stored in the
 transmit queue after a packet has been sent but before an ACK has ... In this example, card 115
 has added FCIP frame header 215, TCP header 220 and **IP header** 225 to form FCIP ...

Related articles - All 8 versions

[PDF] Understanding network processors

N Shah - Master's thesis, University of California, Berkeley, 2001 - Citeseer
 ... this, AAL5 defines its own Packet Data Unit (PDU) with the following contents [6]: · Payload: Higher
 layer PDU, maximum 65,535 bytes · Padding: for AAL5 PDUs to evenly fit into some number of
 ATM cells · 8-byte **trailer**: o User-to ... Figure 7. Internet Protocol (IP) **Header** Format. ...

Cited by 170 - Related articles - All 23 versions

[\[PDF\] from psu.edu](#)[PDF] Proknet c@: Not your average IP over ATM segmenter

R Mason - 2000 - Citeseer
 ... Instruction Set Computer SAP Service Access Point SAR Segmentation and Reassembly SP
 Stack **Pointer** SPR Special ... Thus the SAR has to group any SAR headers and/or **trailers** as well
 as the CS ... to the spec and on the receiver end CRC will be performed on **Trailer** and The ...

Related articles - View as HTML - All 2 versions

[\[PDF\] from psu.edu](#)[PDF] Using Time Division Multiplexing to support Real-time Networking on Ethernet

H Sampathkumar - 2002 - Citeseer
 Page 1. Using Time Division Multiplexing to support Real-time Networking on Ethernet
 by Hariprasad Sampathkumar BE (Computer Science and Engineering), University
 of Madras, Chennai, 2002 Submitted to the Department ...

Cited by 2 - Related articles - View as HTML - Library Search - All 13 versions

[\[PDF\] from psu.edu](#)Static data item processing

BS Sonksen - US Patent App. 09/944,092, 2001 - Google Patents
 ... 304 .300 312 ^ 2nd Processing Module 1 320 328 336" I Output Page 2. Patent Application
 Publication Mar. 6,2003 Sheet 1 of 21 US 2003/0046429 AI Input Output I t 100 **Packet Memory**
 102 Memory Interface 104 Processor z\ 17 106 /_ Processor Memory Fig. ...

All 2 versions

Dynamic data item processing

BS Sonksen... - US Patent App. 09/944,297, 2001 - Google Patents
 ... 304 300 312 320 328 Output 336" Page 2. Patent Application Publication Mar.
 6,2003 Sheet 1 of 21 US 2003/0043800 AI Input Output 100 **Packet Memory** 102
 Memory Interface 104 Processor 106 17 Processor Memory Fig. ...

All 2 versions

Table searching technique

J Kametani - US Patent App. 09/732,402, 2000 - Google Patents
 ... LU 0 Lt I 5 ^" IM rv il — y O o i U co S on ab <C II 1 Q S O ... I / / i i i I IP FLOWS MICRO FLOW 17a
 IP FLOW TABLE V N ST NEXT ADDRESS **POINTER** \ V / ... 20,2003 Sheet 4 of 6 US 2003/0037042
 AI FIG.4 DATA SEARCH START i EXTRACT SEARCH KEY FROM **IP HEADER** Of A ...

All 3 versions

Programmable processing engine for efficiently processing transient data

D Kerr, KM Key, ML Wright... - US Patent 6,513,108, 2003 - Google Patents

... Page 6. US Patent Jan. 28, 2003 Sheet 4 of 10 US 6,513,108 BI LU fy- rv ^- LU CD CD LU 01
CO o Q II LU - o Q iLU eg CO Q_ U_ a: — LO CM ex. LU O o LU 1 I O O C L C C O O r- LU 00 ^- ^-
XScD I— LU CO os < Q LO •* CO o tr o CD O O CO UJ <C (V u_co Q_ Page 7. ...

Cited by 21 - Related articles - All 2 versions

Apparatus and method for using information in one direction of a bi-directional flow in a network to alter characteristics of the return direction flow

RD Maher, JR Deelman... - US Patent App. 10/166,884, 2002 - Google Patents

... Patent Application Publication Dec. 11, 2003 Sheet 7 of 8 US 2003/0227942 AI FIG. 7 POLICY
S COUNTER MAPPINGS 536 SIGNATURE ALGORITHM GENERATOR QOS COMPILER HPP
COMPILER CE SCRIPT COMPILER rv^ NPE IMAGE BUILDER CONTROL I DATA

Cited by 1 - Related articles - All 4 versions

Packet processing system including a policy engine having a classification unit

CE Narad, K Fall, N MacAvoy, P Shankar... - US Patent ..., 2000 - Google Patents

... 5, 2000 Sheet 12 of 19 1110 V DMA Ring Base (as defined in the Ring Array diagram) Invalid
1112 1102 DMA RECOV (pointer register incremented ... 19 RA2 RESVEC 2BIT 1208 DIN1 WI WO
W2 1 1262 BIT-ALU RES-GANG 1240 1254 MEM ADDR 1214"- \ IFx/-~B rr rv y^ _LUJ ...


Cited by 279 - Related articles - All 2 versions

Method and apparatus for end-to-end encryption of a data packet in a computer network

WR Hawe, BW Lampson... - US Patent 5,584,669, 1997 - Google Patents

... 0 K "Z_ _ II1 x "" o_o ^cn g ^ 38 £ cn u »- ^5 £ - X X P nn c> ^ c^t D^A cn P^ x ^ >» ii
O .. Z *~ X Q) a: to x^ T— u!2 u _ X 2 _ j F- X ^ rs UJ rv II • cn z i LZ ^V ^5 h" 1 , - | 1 _
a ^ O O o: 1 O oo o *~ |\$7 m »- o o o K CM 1 - o o *- o o T— a.^_a. ...

Cited by 68 - Related articles - All 2 versions

 [Create email alert](#)

trailer pointer "packet memory" "IP h Search

[Go to Google Home](#) - [About Google](#) - [About Google Scholar](#)

©2010 Google